

# Abstracts

## Numerical Techniques for TRAPATT Circuit Analysis and Design

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*R.D. Regier and K.L. Kotzebue. "Numerical Techniques for TRAPATT Circuit Analysis and Design." 1973 G-MTT International Microwave Symposium Digest of Technical Papers 73.1 (1973 [MWSYM]): 47-49.*

Four numerical techniques used in conjunction with a simple diode model are described for the analysis of TRAPATT circuits. Some of these techniques require minimal computer time and hence appear promising for design purposes.

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